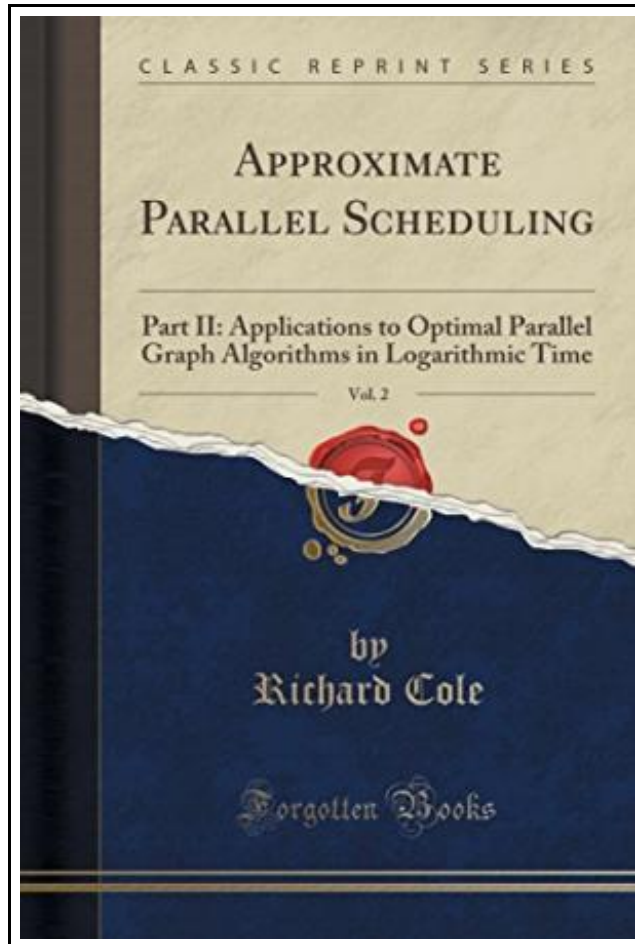# Approximate Parallel Scheduling, Vol. 2: Part II: Applications to Optimal Parallel Graph Algorithms in Logarithmic Time (Classic Reprint)

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Reviews

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APPROXIMATE PARALLEL SCHEDULING, VOL. 2: PART II: APPLICATIONS TO OPTIMAL PARALLEL GRAPH ALGORITHMS IN LOGARITHMIC TIME (CLASSIC REPRINT)

Forgotten Books, United States, 2015. Paperback. Book Condition: New. 229 x 152 mm. Language: English . Brand New Book ***** Print on Demand *****.Excerpt from Approximate Parallel Scheduling, Vol. 2: Part II: Applications to Optimal Parallel Graph Algorithms in Logarithmic Time Part I of this paper presented a novel technique for approximate parallel scheduling and a new logarithmic time optimal parallel algorithm for the list ranking problem. In this part, we give a new logarithmic time parallel (Pram) algorithm for computing the connected components of undirected graphs which uses this scheduling technique. The connectivity algorithm is optimal unless m= o(n log n), in graphs of n vertices and m edges. Using known results, this new algorithms imply logarithmic time optimal parallel algorithms for a number of other graph problems, including: biconnectivity, Euler tours, strong orientation and r-numbering. Another contribution of the present paper is a parallel union/find algorithm. To the best of our knowledge, this paper presents for the first time optimal, logarithmic time, parallel algorithms for problems on graphs. 1. Introduction The models of parallel computation used in this paper are all members of the parallel random, access machine (Pram) family. A Pram employs? synchronous processors all having access to a common memory. An exclusive-read exclusive-write (Crew) Pram does not allow simultaneous access by more than one processor to the same memory location for read or write purposes. A concurrent-read exclusive-write (Crew) Pram allows simultaneous access for reads but not writes. A concurrent-read concurrent-write (Crcw) Pram allows concurrent access for both reads and writes. For the Crcw Pram, we assume that if several processors attempt to write simultaneously at the same memory location then one of them succeeds but we do not know in advance which one. See [Vi-83] for a-survey of results concerning Prams. Let Seq(n) be...

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